

P12665

Patent

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Gilroy J. Vandentop, et al.

Examiner: Zameke, David A.

Serial No.: 10/020,911

Art Unit: 2827

Filing Date: December 19, 2001

For: ELECTRICAL/OPTICAL
INTEGRATION SCHEME USING
DIRECT COPPER BONDING

Mail Stop Appeal Brief - Patents
Commissioner for Patents
P.O Box 1450
Alexandria, VA 22313-1450

APPEAL BRIEF UNDER 37 C.F.R. § 1.192

Dear Sir:

Appellants hereby submit this Brief in triplicate in support of Appellants' appeal from a final decision of the Examiner mailed April 6, 2004 in the above-referenced application. Appellants respectfully request consideration of this Appeal Brief by the Board of Patent Appeals and Interferences for allowance of the above-referenced application.

REAL PARTY IN INTEREST

The real party in interest is Intel Corporation, a Delaware Corporation

having a principal place of business in Santa Clara, California.

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minimized. As already mentioned, Fan demonstrates no appreciation for optical losses or the usefulness of direct copper bonding in minimizing these losses. Indeed, Appellants assert that one of ordinary skill would not normally consider a purely electrical technique to provide advantages in the optical domain as claimed. Accordingly, the art can support no implicit motivation to modify Fan to minimize optical losses in an electro-optic semiconductor package. There must be an express suggestion to combine optics with direct copper bonding before a hypothetical worker would do so. For at least the above reasons, Appellants request that the Board reverse the rejection of claim 31.


SUMMARY

In summary, Appellants respectfully submit that all of the claims on appeal are patentable over the references cited in the Final Office Action, and respectfully request a favorable decision by the Board.


If there are any charges, please charge Deposit Account No. 50-0221.

Respectfully submitted,

Dated: 8/5, 2004


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